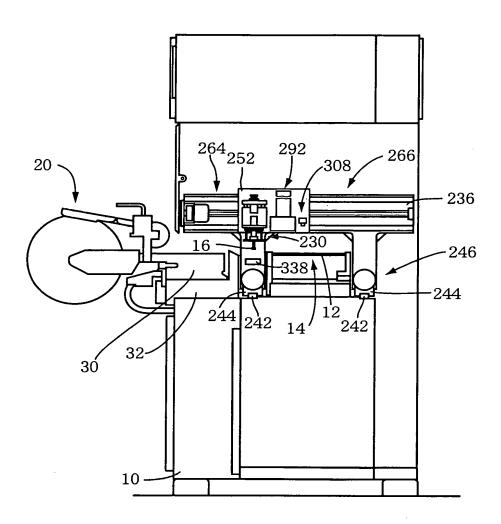
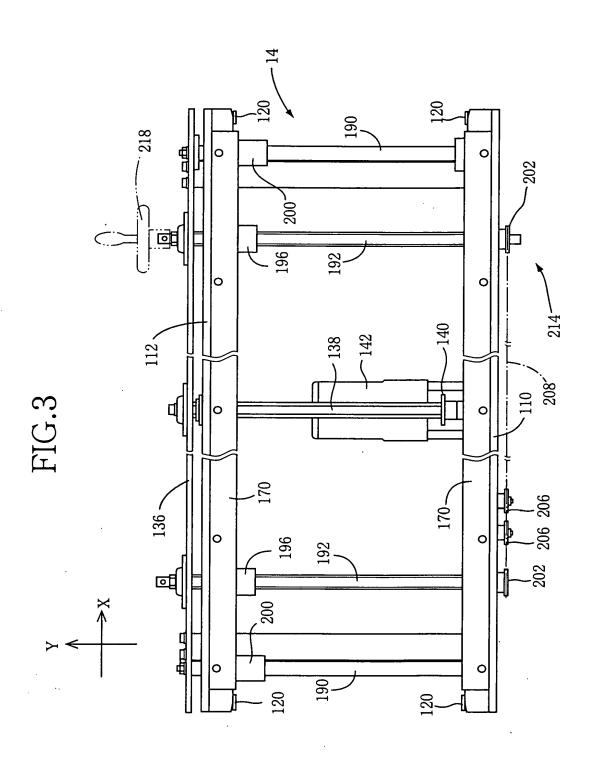
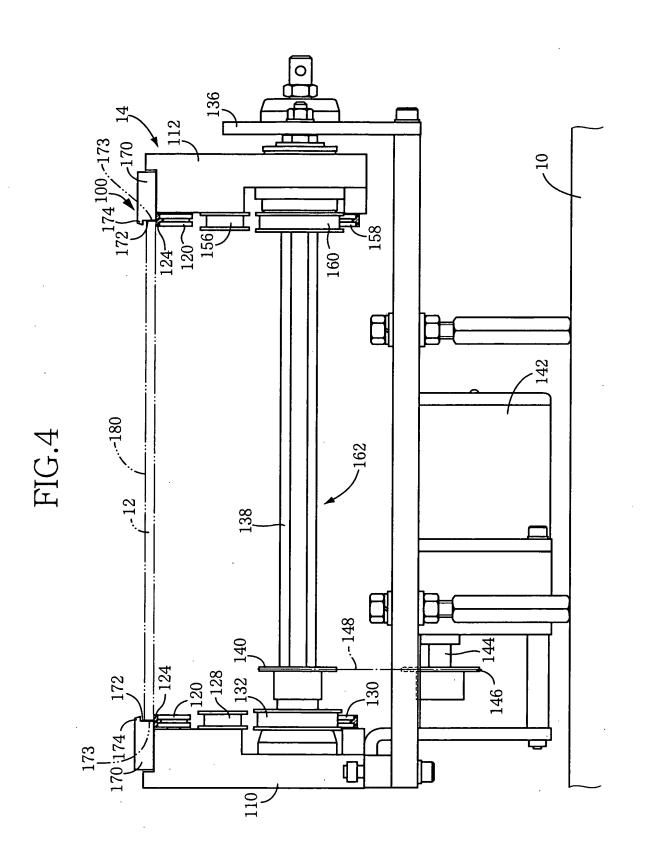
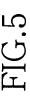


FIG.2









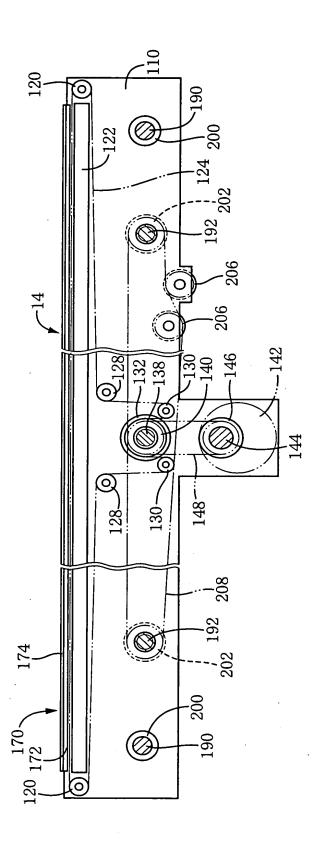


FIG.6

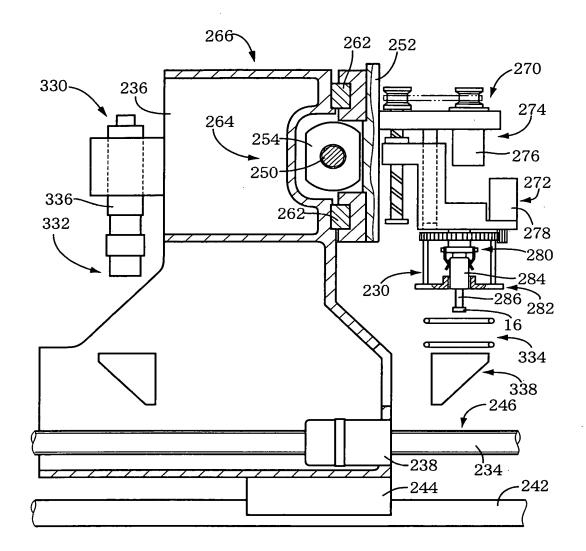


FIG.7

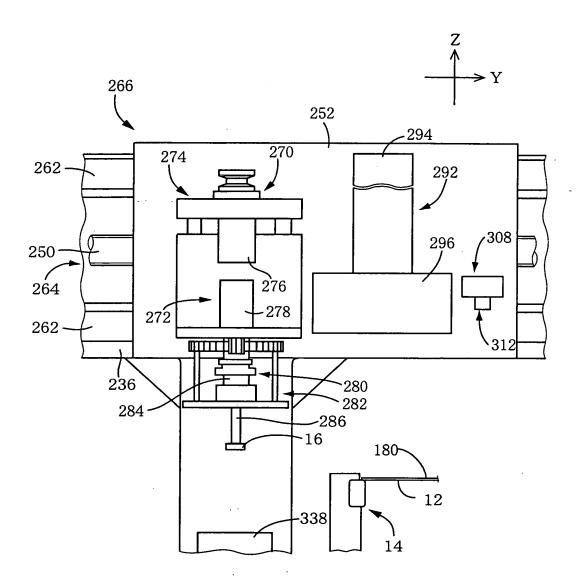
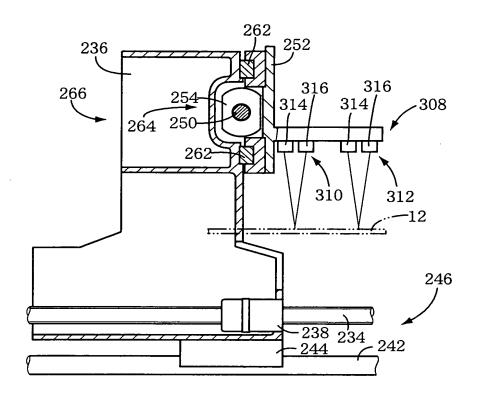


FIG.8





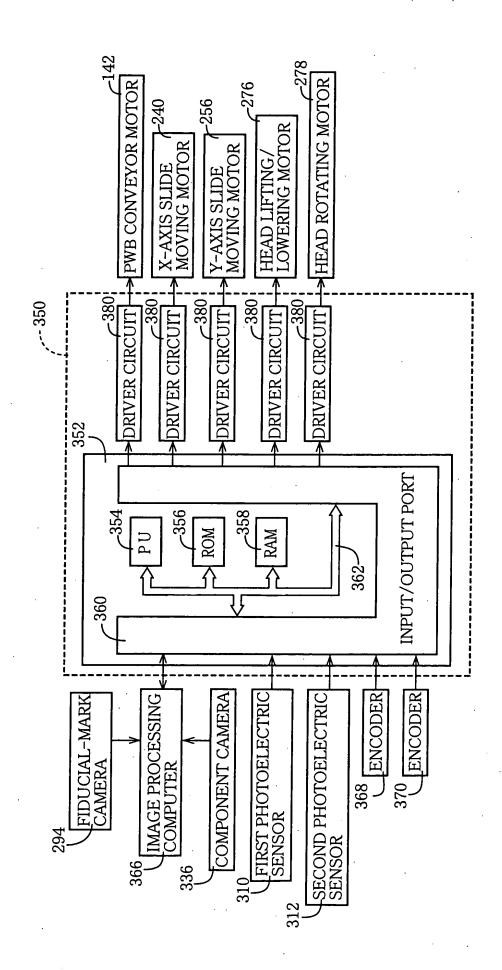
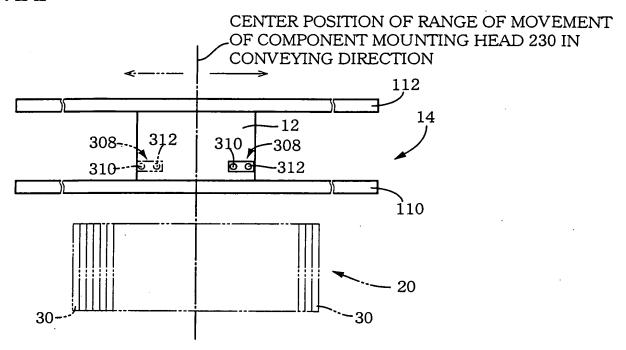
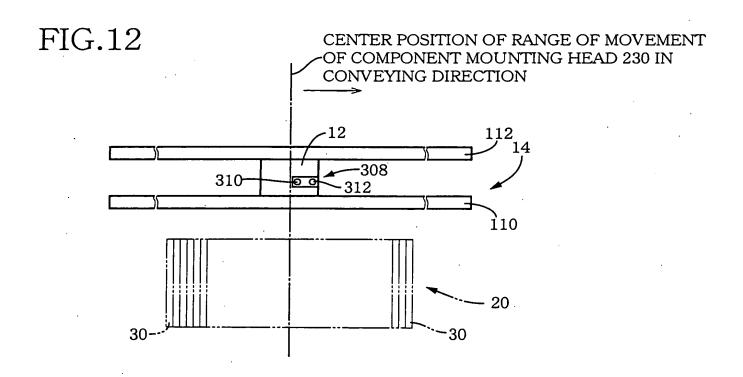


FIG.10

RAM	
PROGRAM MEMORY	
PWB STOP POSITION DATA MEMORY	
PWB DETECTION LOCATION DATA MEMORY	

FIG.11





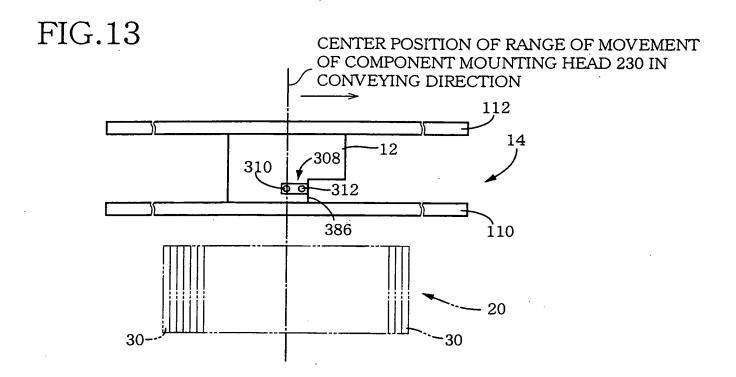
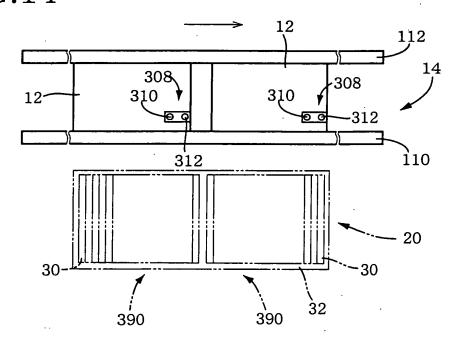
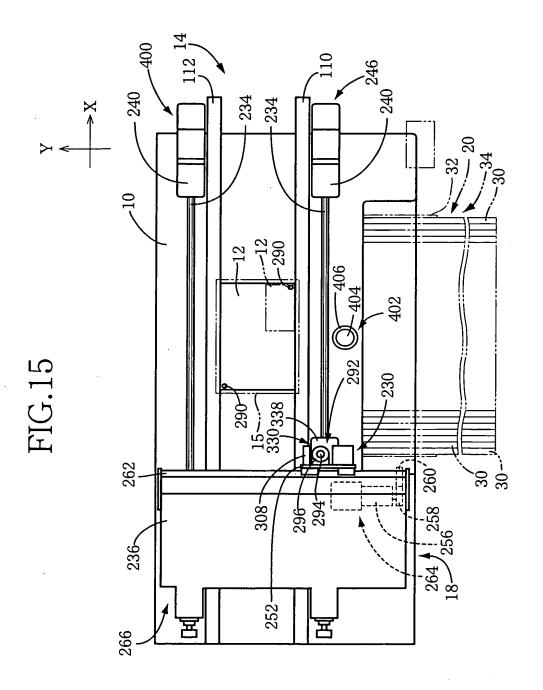


FIG.14





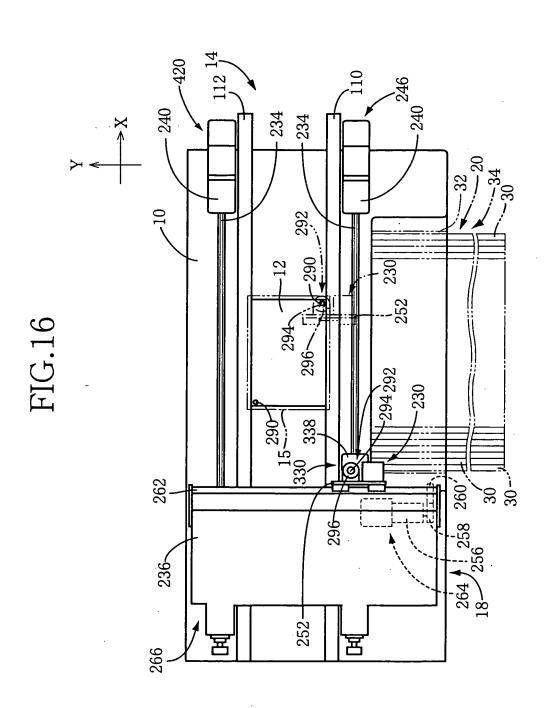


FIG.17

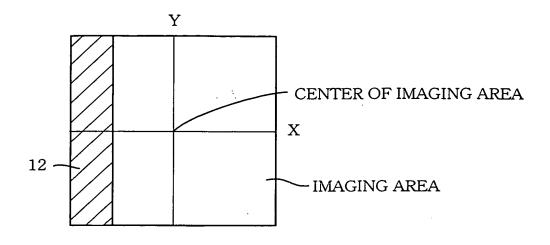


FIG.18

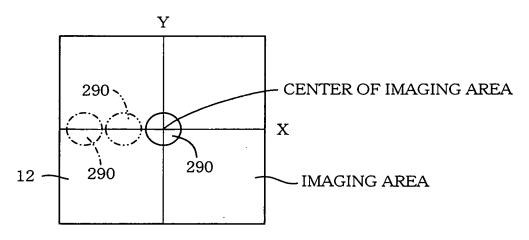
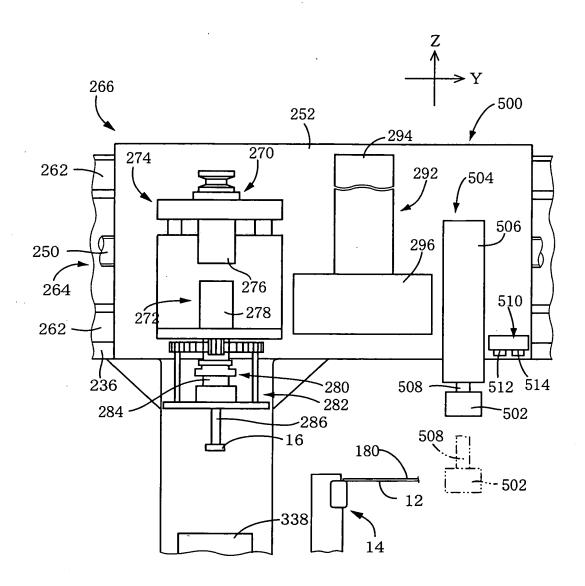
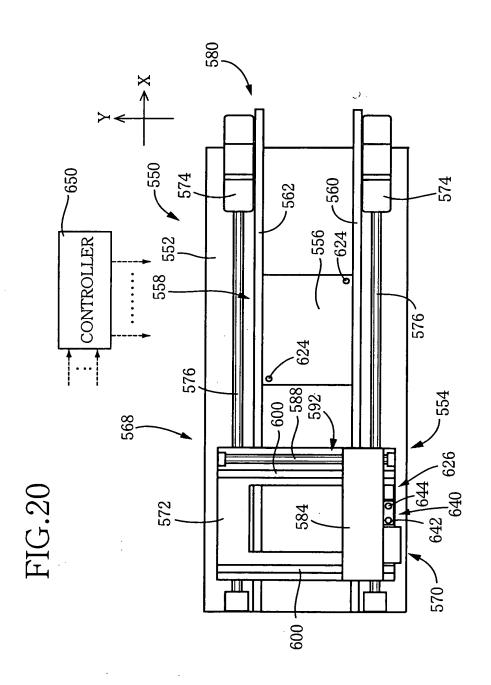


FIG.19





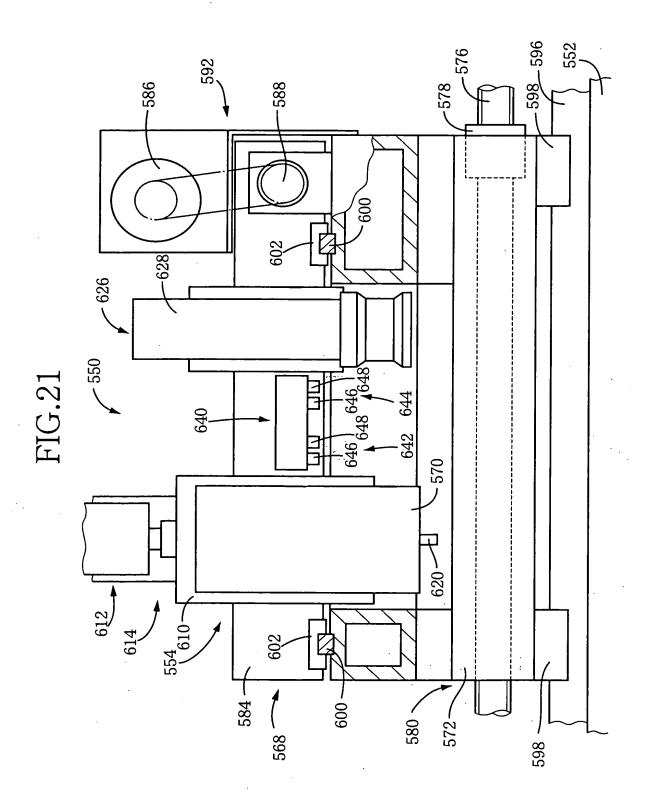


FIG.22

